

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated October 1, 2007, has been received and its contents carefully reviewed.

Claims 2, 4-6, 10, 16-20, 23 and 25-30 are rejected by the Examiner. Claims 2-6, 10, and 16-18 have been amended and claim 15 have been canceled. Claims 2-6, 9-11, 16-20 and 23-30 remain pending in this application. Reexamination and reconsideration of the pending claims is respectfully requested.

In the Office Action, claim 2 is rejected under 35 U.S.C. § 103(e) as being anticipated by U.S. Publication No. 2001/0005240 to Takeishi (hereinafter "Takeishi"). Claim 4-6, 10, 16-20, 23 and 25-30 is rejected under 35 U.S.C. § 103(a) as being unpatentable over Takeishi in view of U.S. Patent No. 6,636,281 to Kanatsu (hereinafter "Kanatsu"). Claim 9 is allowed. Claims 3, 11, 15 and 24 are objected to be being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitation of the base claim and any intervening claims.

Claim 2 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a printed circuit board with a variable resistor (VR)"; "a shield cover covering the printed circuit board with the variable resistor"; wherein the shield cover includes a structure arranged over the variable resistor that is openable for adjusting the common voltage"; and "wherein the structure includes a U-shaped opening in the shield cover around the variable resistor, such that the variable resistor is selectively exposable by the structure" None of the cited references including Takeishi, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 2 is allowable over the cited references.

Claim 4 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, “a printed circuit board with a variable resistor (VR)”; “a shield cover covering the printed circuit board with the variable resistor”; a module connector formed on the printed circuit board”; “a board connector connected with the module connector”; “wherein the shield cover includes a structure arranged over the variable resistor that is openable for adjusting the common voltage and an opened part arranged over the module connector and the board connector is inserted so as to be connected with the module connector.” None of the cited references including Takeishi and Kanatsu, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 4 and claims 5-6 depending from claim 4 are allowable over the cited references.

Claim 10 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, that “a shield cover comprises a covering structure over the PCB, the covering structure comprising at least one elastically deformable region; a slit formed in the covering structure over the PCB and a board connector is inserted so as to be connected with the PCB; and a variable resistor formed on the PCB, the shield cover further comprising a flap in, the covering structure over the variable resistor”. None of the cited references including Takeishi and Kanatsu, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 10 and claim 11 depending from claim 10 are allowable over the cited references.

Claim 16 is amended to including a allowable claim 15, and hence the amended claim 16 is allowable over the cited references

Claim 17 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, that “a shield cover comprises a slit formed

in the shield cover a printed circuit board and a board connector is inserted so as to be connected with the PCB”. None of the cited references including Takeishi and Kanatsu, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 17 and claim 19-20 depending from claim 17 are allowable over the cited references.

Claim 23 is allowable over the cited references in that each of these claims recites a combination of elements including, for example, that “a variable resistor on the PCB; a shield cover on the PCB, the shield cover including the at least one elastically deformable region includes a flap formed over the variable resistor”. None of the cited references including Takeishi and Kanatsu, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claim 23 and claim 25-30 depending from claim 23 are allowable over the cited references.

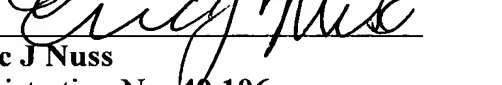
Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. *A duplicate copy of this sheet is enclosed.*

Respectfully submitted,

Dated: **2 January 2008**

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